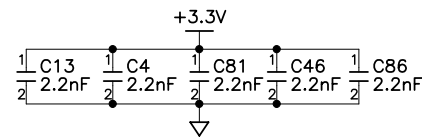
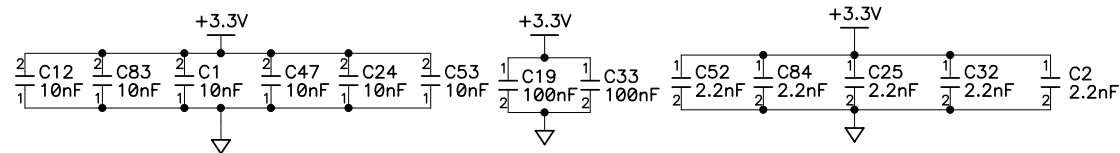
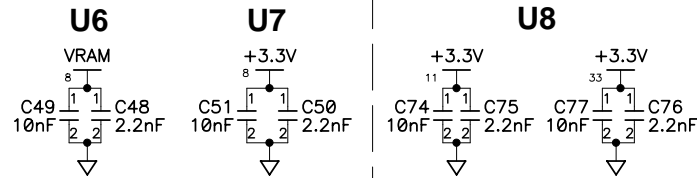


POWER TABLE

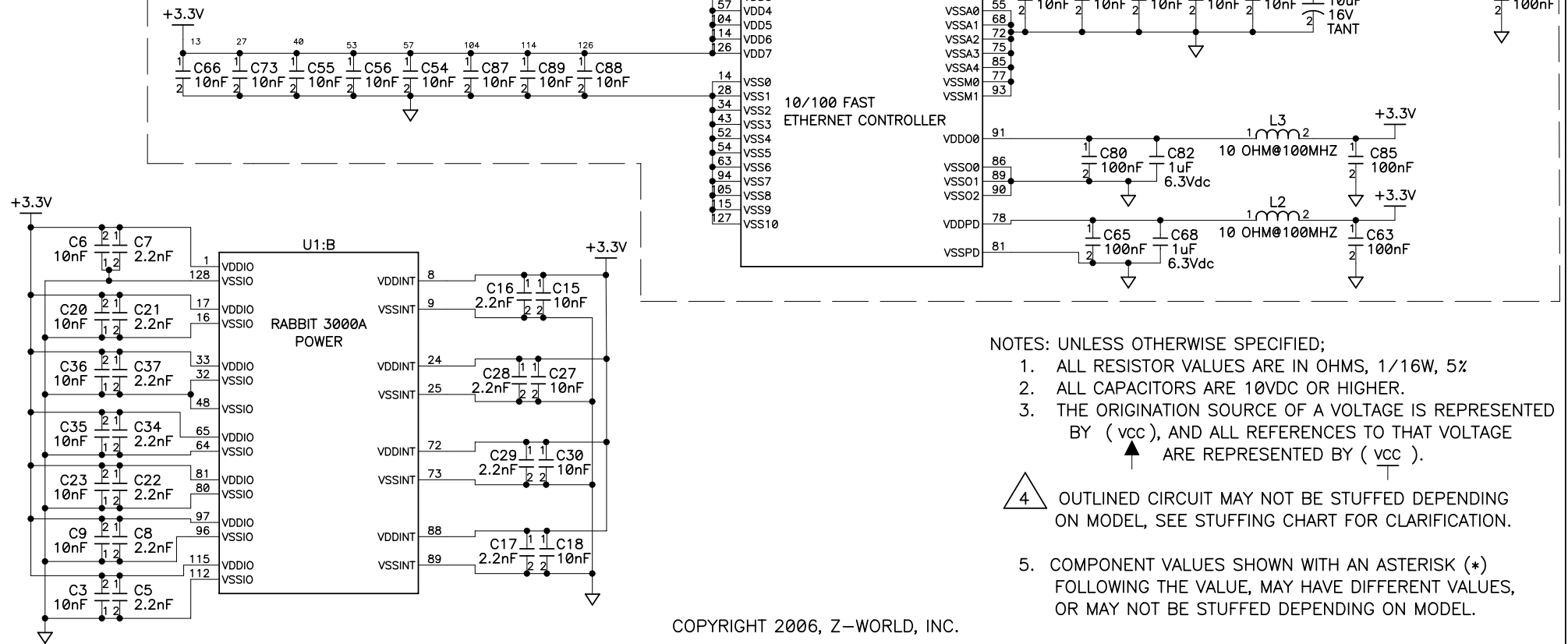
Ref Des	Device	GND	+3.3V	VRAM
U2	74LCX245	10	20	
U3	74ALVC32	7	14	
U6	SRAM 1	24		8
U7	FLASH	24	8	
U8	SRAM 2	34,12	33, 11	



STUFFING TABLE

CIRCUIT	PART	RCM3200	RCM3210	RCM3220
SRAM 1	U6	256K	128K	256K
SRAM select	JP5	1-2	1-2	1-2
SRAM 2	U8	512K	NOT INSTALLED	512K
SRAM 2 CS	JP2	2-3	NOT INSTALLED	2-3
FLASH	U7	512K	256K	512K
FLASH select	JP3	2-3	1-2	2-3
BANK SELECT	JP4	1-2	1-2	1-2
EXT DATA BUS	JP1	2-3	2-3	1-2
POWER TO VRAM	R41	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
OSCILLATOR CIRCUIT	Y1	NOT INSTALLED	14.74 MHz	NOT INSTALLED
	Y2	22.1184 MHz	NOT INSTALLED	22.1184 MHz
	C10, C11, R15	INSTALLED	INSTALLED	INSTALLED
PORT D OPTION	R29, R37, R39, R40	INSTALLED	INSTALLED	INSTALLED
LED OPTION	R67, R70	INSTALLED	INSTALLED	NOT INSTALLED
FUTURE OPTION	R45, R46, R53, R57, R74	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
INTERRUPT OPTION	R61	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
RTC	U11	INSTALLED	INSTALLED	INSTALLED
	R28, R80	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
CS OPTION	R58	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED
Decoupling Caps	C74, C75, C76, C77	INSTALLED	NOT INSTALLED	INSTALLED
ETHERNET OPTION	See sheets 1 and 3	INSTALLED	INSTALLED	NOT INSTALLED

ETHERNET OPTION



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APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE	
		DRAWN BY: (INITIAL RELEASE)	DARREN MUSGROVE	SCHEMATIC DIAGRAM	
		REVISD BY:	T.PHAN	RCM3200 SERIES	
		APPROVALS: INITIAL RELEASE		CORE MODULE	
		PROJECT ENGINEER:	DARREN MUSGROVE	SIZE	DWG NO.
		ENGINEERING MANAGER:	R. MATTHEWS	<b>B</b>	090-0152
		SIGNATURES	DATE	SCALE	NONE
				RELEASE DATE	26JUL02
				SHEET	1 OF 3

